ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Accurate Generation of Scan Enable Signal when Testing Integrated Circuits Using Sequential Scanning Techniques

Submision Type : Utility Patent Filing

Application Number:

10/709240

EFS ID: 59510

Server Response:

59510				
Co	nfirmation			

Confirmation Code	Message		
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First Named Applicant: Hasanuzzaman SHEIKH

Attorney Docket Number: TI-36330

Timestamp: 2004-04-23 09:19:40 EDT

From: us

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